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Title: SYSTEM AND METHOD FOR DATA RETENTION WITH REDUCED LEAKAGE CURRENT

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IN THE DRAWINGS

Corrected and formalized drawings are supplied herewith, each labeled as "REPLACEMENT SHEET".

FIG. 4 has been amended to show the current path through the well of FET 432 from supplemental supply voltage (node 434) that occurs during standby mode. Well 540 and well tap 538 are also now shown.